

What is claimed is:

1. A display apparatus, comprising:

a plurality of display panels, each showing different
5 displays;

a single display panel driving unit for commonly
operating the display panels; and

a connection means for physically and electrically
inter-connecting the display panel driving unit with the
10 display panels.

2. The display apparatus as recited in claim 1, wherein
the display panels includes a first display panel and a second
display panel, and rear sides of the first and the second
15 display panels face each other with the display panel driving
unit disposed between the first and the second display panels.

3. The display apparatus as recited in claim 1, wherein
the connection means is formed by using one method selected
20 from a group consisting of a tape carrier package (TCP) method,
a chip on film (COF) method, a chip on board (COB) method and
a chip on glass (COG).

4. The display apparatus as recited in claim 1, wherein
25 the display panel driving unit comprises a plurality of
display panel drivers for operating each of the display panels
and a switching unit for switching the display panel drivers.

5 5. The display apparatus as recited in claim 1, wherein
the display panel driving unit further comprises a display
path control unit for controlling a path connected with a
display panel drive pad of a selected display panel in order
to operate the selected display panel.

6. The display apparatus as recited in claim 5, wherein
the display panel driving unit further comprises:

10 a CPU interface control unit for controlling
constitution elements included in the display panel driving
unit by receiving a command from an external host or a central
processing unit;

15 a display panel control unit for controlling the display
panel with an external control signal transmitted through the
CPU interface control unit or an independent port;

20 a memory unit for storing data displayed on the display
panels;

25 an X or an Y address decoder for selecting a
corresponding address of the memory unit by decoding an
encoding signal outputted from the display panel control unit;

 a register unit for informing each independent operation
condition of the display panels;

 a timing control unit for controlling a point of time
for decoding, latching and displaying a data for the selected
display panel by the information obtained from the register
unit;

 a line address decoder for decoding an address for the

data of the corresponding display panel at a line unit by responding to an output of the timing control unit;

a latch unit for latching the data corresponding to the address decoded at the line unit, wherein the data is transferred from the memory unit; and

a voltage generation unit for supplying a power voltage for operating each display panel.

7. The display apparatus as recited in claim 6, wherein the display panels share the X and the Y address decoders, the line address decoder, the voltage generation unit, the memory unit and register unit during a concurrent and cooperative operation.

8. The display apparatus as recited in claim 6, wherein the voltage generation unit comprises a voltage converter and a DC/DC booster controlled by on-off states of the first and second display panels.

9. The display apparatus as recited in claim 9, wherein a clock frequency of the DC/DC booster is adjusted and the size of a transistor of the DC/DC booster is reduced in case of operating a large display panel of the display panels, and an output state of the memory unit 401 or the address decoder is decided according to on-off operation states of the first and the second display panels and consequently, the memory unit is partially accessed.